

## Programming Manual for MCC





**Programming Manual MINILOG  
for the Controllers  
MCC-1, MCC-2 and MCC-2 LIN**

**TRANSLATION OF THE GERMAN ORIGINAL MANUAL**

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Every possible care has been taken to ensure the accuracy of this technical manual. All information contained in this manual is correct to the best of our knowledge and belief but cannot be guaranteed. Furthermore we reserve the right to make improvements and enhancements to the manual and / or the devices described herein without prior notification.

We appreciate suggestions and criticisms for further improvement. Please send your comments to the following E-mail address: [doku@phytron.de](mailto:doku@phytron.de)

## Contents

1	Structure of the Minilog Instructions .....	4
1.1	Instruction Code .....	4
1.2	Design of MiniLog Programs .....	5
1.3	Addressing Mode .....	5
1.4	Conditional Instructions .....	6
1.5	Data and Telegram Format .....	7
2	MINILOG Instructions .....	9
2.1	Outputs .....	9
2.2	A/D Converter .....	10
2.3	Reset .....	10
2.4	Write Instructions via Serial Interface .....	10
2.5	Input requests .....	11
2.6	Program Manipulation at Emergency Stop (ONLY PROG) .....	13
2.7	Program Interruption .....	13
2.8	System Adaption during Program Execution .....	13
2.9	Jump Instructions (ONLY PROG) .....	15
2.10	Repeating of Program Lines .....	16
2.11	Password .....	17
2.12	Ending or Interruption of a Program Call (ONLY PROG) .....	17
2.13	Program and Data Management (ONLY PC) .....	18
2.14	Registers .....	20
2.15	Register Instructions .....	21
2.16	System Status (ONLY PC) .....	29
2.17	Store Data into Flash EPROM .....	31
2.18	Time Loops .....	31
2.19	Subroutines (ONLY PROG) .....	32
2.20	Terminal Instructions (also by PC in case of terminal connection) .....	33
2.21	Axes Instructions .....	34
2.22	Function Keys Read Out on Terminal BT24 (also by PC) .....	38
3	List of Minilog Instructions .....	39
4	List of DIN Instructions .....	43
5	Parameters .....	46
5.1	List of Parameters .....	47
5.2	Parameter Set Transmission to the Controller .....	51
6	Programming Example .....	52
6.1	General .....	52
6.2	A/D Converter .....	52
7	Storing Programs, Parameters and Registers .....	53
8	Current Shaping CS .....	54
9	Index .....	55

## 1 Structure of the Minilog Instructions

### 1.1 Instruction Code

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Xrvalue	<b>X</b>	<p>The bold characters represent the instruction code and must be used unchanged.</p> <p>In this example: <b>X</b> represents the motion instruction code for relative positioning of the X-axis.</p> <p><b>This manual shows only the instruction codes for the single axis controller (MCC-1), where the axis is generally named “X”.</b></p> <p><b>For multiple axis controllers (also Master/slave up to 8 axes) the corresponding characters X, Y,... or 1, 2, ... must be used.</b></p>
	r	<p>Small letter require the input of the characters or values which are described in the column <i>Meaning</i>.</p> <p>In this example: r = running direction + or – .</p>
	value	<p>In this example a running distance of 1000 is fed in. The corresponding unit (e.g. steps) of the particular input is defined by <b>parameters</b>. For the specific parameters refer to chap. 5.</p>

Example: X+1000

Relative motion instruction for the X-axis:  
Go 1000 steps to the + direction.

**Important:**

- **All characters and signs, belonging to one single instruction, must be written without a blank.**
- **The instructions themselves must be separated by a blank.**
- **Leading zeros in an instruction are ignored (Example: the instruction A001S is realized as A1S)**
- **Instructions, which cannot be used in the program and direct mode, are marked with:**
  1. **Instruction only used in the program (ONLY PROG)**
  2. **Instruction only used in the PC direct mode (ONLY PC)**

**Exception:** In the instruction group “System Status (ONLY PC)”, chapter 2.12, the first program name must be separated by a blank from the second program name or the following alphanumeric part of the instruction code.

## 1.2 Design of MiniLog Programs

- MiniLog programs consist of up to 2000 program lines. The program lines are numbered consecutively by MiniLog-Comm.
- The single instructions in each line must be separated by blank characters.
- Do not insert extra blank characters within an instruction.
- The instructions will be executed serially.
- By means of the line numbers jump instructions or subroutines can be defined.
- Parameter and register values should be defined at the beginning of a program.
- Line, parameter and register numbers may be entered with or without preceding zeros.

Example: R0001 or R1

- A line break is used in the program by a carriage return (**CR**) : **0x0D**

Example: A1S T500 A1R **0x0D**  
A2S T500 A2R **0x0D**

All programming instructions are assembled in the MiniLog programming manual.

## 1.3 Addressing Mode

For instructions, where at least one register is used as an operator, two addressing modes are available: The **Direct Addressing Mode** and the **Indirect Addressing Mode**. In this programming manual the meaning of the basic instruction is always explained for the **Direct Addressing Mode**. The variations of the **Indirect Addressing Mode** are listed for the sake of completeness. The first named register within an instruction code is always the destination register for the result.

### Example for Direct Addressing Mode:

<u>Instruction</u>	<u>Meaning</u>
RnnBE <sub>nn–mm</sub>	The status of the inputs nn to mm is written as a binary value into the register nn.
	<b>Example: R1BE1–8</b>
	Status of the inputs 1 to 8 is e.g. <b>1010 0101</b> . This binary value is written into the register 1. After the Instruction was carried out, the register content is 165 decimal.

## MINILOG

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### Example for Indirect Addressing Mode:

<u>Instruction</u>	<u>Meaning</u>
R[Rnn]BE <sub>nn-mm</sub>	<b>Indirect Addressing Mode:</b> The status of the inputs nn to mm is written as a binary value into the register which is addressed by the register [Rnn].  Example: <b>R1S10 [R1]BE1-8</b>  The addressing register [R1] is set to 10. The status of the inputs 1 to 8 is e.g. 1010 0101. This binary value is written into the register 10, which was addressed by the register 1. After the instruction was carried out, the content of the register 10 is 165 decimal.

### Addressing with Label

In case of jump calls (page 15) and subroutine calls (page 32) the start or destination line can be set in the instruction code with a label (\*la\*), which is assigned to this program line. A label is defined between two \* and can have up to 6 alphanumeric signs. Max. 100 labels can be used in one program.

Example: \*[label name]\*

### Program name:

Program names [name] in the instruction code can have up to 8 alphanumeric signs.

## 1.4 Conditional Instructions

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The execution of some instructions (e.g. jumps or subroutine calls) can be combined with a condition. Before a conditional jump etc. can be used, the condition byte has to be set, for example by an input request (see chap. 2.5) or a register comparison (see chap. 2.14).

Possible states of the condition byte:

**E** = Condition fulfilled    **N** = Condition not fulfilled

The state of the condition byte remains stored until it is changed again.

All instructions which set no condition delete the condition request.



## 1.5 Data and Telegram Format

**Data format:** No Parity  
 1 Stopbit  
 8 Bit ASCII-Code  
 57 600 Baud

The **send telegram** from PC via RS232 is defined as:

**Without check-sum:** <STX> | Address | Instruction | <ETX>

**With check-sum:** <STX> | Address | Instruction | Separator | Checksum | <ETX>

The **response telegram** (always for address 0-9, A-F) is defined as:

<STX> | ACK | Answer | <ETX> or  
 <STX> | ACK | <ETX> or  
 <STX> | NAK | <ETX>

	Meaning
<STX>	<STX> (Start of Text, 02 <sub>H</sub> ): It is exclusively used as the start code for a new telegram
Address	Address of the controller, the range of the address byte is 0 to 9 and A to F (30 <sub>H</sub> ...39 <sub>H</sub> and 41 <sub>H</sub> ...46 <sub>H</sub> ). Additional the Broadcast <sup>1</sup> address @ (40 <sub>H</sub> ) is used.
Instruction	MINILOG instruction code
Separator	: Colon (3A <sub>H</sub> ) as separator, to distinguish between usable data and checksum.
Checksum	Upper byte of the checksum value (see below for the algorithm to calculate the checksum)
	Lower byte of the checksum value (calculation see below)
<ETX>	(End of Text, 03 <sub>H</sub> ) this code indicates the end of the telegram.
ACK	(Acknowledge 06 <sub>H</sub> ), the instruction has been confirmed.
NAK	(Negative Acknowledge 15 <sub>H</sub> ), the instruction has been negatively confirmed.
Answer	Answer as number or string, f.ex. E or N

<sup>1</sup> Broadcast: All axes receive and evaluate the telegram. To avoid bus-conflicts caused by the response of all axes nearly within the same time, the response of the controllers is suppressed by addressing with “@”.

## MINILOG

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The checksum CS is defined by summing up all bytes, beginning with the address byte and including the separator (:) in an exclusive-OR-operation:

$$\text{CS} = \text{address} \oplus \text{data byte 1} \oplus \text{data byte 2} \dots \oplus \text{data byte n} \oplus \text{separator}$$

The checksum is calculated as one 8-bit binary value (00<sub>H</sub> to FF<sub>H</sub>). This byte is taken apart in its upper and lower byte (nibbles). After the HEX values of the two nibbles have been transferred to the corresponding two ASCII characters (0 to 9 instead of 0<sub>H</sub> to 9<sub>H</sub> and A to F instead of A<sub>H</sub> to F<sub>H</sub>, that means to each nibble 30<sub>H</sub> or rather 37<sub>H</sub> is mathematically added), the checksum is written in the telegram.

The MCC also calculates (Exclusive OR) the checksum of the received data. The telegram will be rejected if a difference to the received checksum is detected, and the error is confirmed by NAK.

If there is no need to validate the contents of the telegram, the checksum monitoring can be set off. Instead of the checksum bytes, **two X** characters will be accepted, e. g.:

<STX> | 1 | X | + | 1 | 0 | 0 | : | X | X <ETX>

## 2 MINILOG Instructions

### 2.1 Outputs

<u>Instruction</u>	<u>Meaning</u>
	<b>Set Outputs</b>
<b>Annnz</b>	Set one or several outputs at the same time. nnn, mmm, xxx → number (ID) of the output
<b>Annnzmmmzxxxz</b>	z = S → set z = R → reset <b>Example: A1S2R3S</b> Output 1 and 3 ON, output 2 OUT
	<b>Read Output Status</b>
<b>AGnR</b>	Read the state of the output groupes n. (ONLY PC) <b>Example: AG2R</b> The 2nd output groupe is read <b>Response : &lt;STX&gt;&lt;ACK&gt;nnnnnnnn&lt;ETX&gt;</b> (ONLY PC) n = 0 Output OFF n = 1 Output ON
	<b>Set the output group outputs</b>
<b>AGnSzzzzzzzz</b>	Set the output group n=1 or 2, z= 0 or 1. z must always have 8 places <b>Example: AG1S10101001</b> The 1. output group is set with the information '10101001'
	<b>Read Output Status</b>
<b>ARnnn;mmm;xxx</b>	The state of the outputs nnn, mmm, xxx is read. (ONLY PC) <b>Response : &lt;STX&gt;&lt;ACK&gt;nnn&lt;ETX&gt;</b> n = 0 Output OFF n = 1 Output ON <b>Important:</b> Set a ; between the output numbers.

# MINILOG

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## 2.2 A/D Converter

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<u>Instruction</u>	<u>Meaning</u>
ADnR	A/D converter setting is read. n → A/D converter address: n=1 or n=2  <b>Response :</b> <STX><ACK>[0 to 1023]<ETX> (ONLY PC) 0 to 1023 = 0 to 5 V

## 2.3 Reset

---

<u>Instruction</u>	<u>Meaning</u>
CR	The controller is reset by the interface.
CT	The display of the terminal is deleted via interface.  <b>Response :</b> <STX><ACK><ETX> (ONLY PC)

## 2.4 Write Instructions via Serial Interface

---

<u>Instruction</u>	<u>Meaning</u>
	Informations can be carried out via the 3 serial interfaces (X31, X32, X9). The writing is carried out without formatting. s = 1 → interface name RS232 /Com X5)
Ds <text>	The bracketed expression is carried out.
DsRnn	The content of the register nn is carried out.
DsR[Rnn]	The content of the register which is addressed by register nn is carried out.
DsxPmm	Parameter mm of the axis x is carried out. mm = 1 to 45 → number (ID) of the parameter x = 1 to 8 or X,Y,Z,W,5,6,7,8 → axis ID  <b>Example: D24P10</b> The parameter 10 of the axis 4 is carried out via the interface X32 .

## 2.5 Input requests

<u>Instruction</u>	<u>Meaning</u>
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### Logical AND

<b>E<sup>^</sup>nnzmmzxxz</b>	<p>The inputs nn, mm, xx are tested as AND condition.            Only if the AND condition is fulfilled the condition byte is set.            Otherwise the condition byte is reset.</p>
-------------------------------	--

nn. mm. xx → input number

z = S → input ON

z = R → input OFF

**Example: E<sup>^</sup>1S2R3S**

The input states 1, 2 and 3 are read out. If input 1 is set, input 2 reset and input 3 set, the AND condition is fulfilled and the condition byte is set. Now a conditional jump or a conditional call of a subprogram can be carried out.

**Response: <STX><ACK> E <ETX> or  
 <STX><ACK> N <ETX> (ONLY PC)**

### Logical OR

<b>Evnnzmmzxx</b>	<p>The inputs nn, mm, xx are tested as OR condition.            Only if the OR condition is fulfilled the condition byte is set.            Otherwise the condition byte is reset.</p>
-------------------	--

nn. mm. xx → input number

z = S → input ON

z = R → input OFF

**Example: Ev1S2R3S**

The input states **1, 2 and 3** are read out. If input 1 is set or input 2 reset or input 3 set, the AND condition is fulfilled and the condition byte is set. Now a conditional jump or a conditional call of a subprogram can be carried out.

**Response: <STX><ACK> E <ETX> or  
 <STX><ACK> N <ETX> (ONLY PC)**

### Wait for Condition Fulfilled

<b>Ennz</b>	<p>Wait for the preset input condition.            The program stops until the preset input condition is fulfilled. The condition byte is not affected. (ONLY PROG)</p>
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# MINILOG

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## **Instruction**

## **Meaning**

**Ennzmmz**

When reading the status of several inputs, one input after the other is read out (no AND linking). The condition byte is not affected. (ONLY PROG)

**Example: E1S2R3S**

The status of the inputs 1, 2 and 3 are read.  
After the input 1 is set, the input 2 is read. After the input 2 is reset, the input 3 is read. After the input 3 set, the reading Instruction is done and the program continues. After the instruction end the inputs 1 and 2 can have another state.

### **Definition of the inputs and outputs only for MCC-1**

**EASnnnnnnnn**

The MCC-1 controller has eight digital inputs and outputs, electrically insulated and bidirectional. Which I/Os are input or output can be defined by the user via MiniLog programming.

n = allocation → input or output

n = 1 → input

n = 0 → output

**Example: EAS0000011**

1 to 6 are outputs, 7 and 8 are used as inputs.

### **Read input group**

**EGnR**

The input group n is read. (ONLY PC)

n=1 to 8

**Response : <STX><ACK>nnnnnnnn<ETX>**

n = 0 input is reset

n = 1 input is set

**ERnn;mm;xx**

The Status of the inputs nn, mm, xx is read (ONLY PC).

**Response: <STX><ACK>nnn<ETX>**

n = 0 input is reset

n = 1 input is set

**Important:** Set a ; between the input numbers.

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## 2.6 Program Manipulation at Emergency Stop (ONLY PROG)

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<u>Instruction</u>	<u>Meaning</u>
<b>FNznr</b>	The program line is defined at which the program has to be continued in the case of an emergency stop.
<b>FN*la**</b>	The program line, at which the program has to be continued in the case of an emergency stop, is defined by a label.
<b>FP[name]</b>	Indicates the program for an emergency stop. In the case of an emergency stop, a jump to the named program is initiated.

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## 2.7 Program Interruption

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<u>Instruction</u>	<u>Meaning</u>
<b>H</b>	The program waits here until all axes have stopped. (ONLY PROG)

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## 2.8 System Adaption during Program Execution

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<u>Instruction</u>	<u>Meaning</u>
	<b>Number of Axes</b>
<b>IAR</b>	The number of existing axes is requested (ONLY PC). <b>Response: &lt;STX&gt;&lt;ACK&gt;n&lt;ETX&gt;</b>
	<b>Automatic Start</b>
<b>IBSname</b>	The name of the start program is written into the Auto Start register. If the REMOTE/LOCAL switch is in the LOCAL position the program execution starts here. <b>Response: &lt;STX&gt;&lt;ACK&gt;&lt;ETX&gt; or &lt;STX&gt;&lt;NAK&gt;&lt;ETX&gt; (ONLY PC)</b>
<b>IBR</b>	The name of the Auto Start program is read (ONLY PC). <b>Response: &lt;STX&gt;&lt;ACK&gt;name&lt;ETX&gt;</b>
	<b>Read/Set Baudrate (ONLY PC)</b>
<b>ICnSbaud</b>	Set the baudrate for the MCC interfaces. n = 1 → COM 1 of MCC baud = Baudrate (9600, 19200, 38400, 57600 or 115200 Baud)
<b>ICnR</b>	Baudrate setting of the MCC interfaces is read out. n = 1 → COM 1 of MCC

## MINILOG

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<b><u>Instruction</u></b>	<b><u>Meaning</u></b>
	<b>Remote/Local Reversing (ONLY PC)</b>
<b>IFR</b>	The controller is reversed to the Remote function. If a program is running, it is canceled. If the switch is positioned to Local, the position Remote is simulated. <b>Response: &lt;STX&gt;&lt;ACK&gt;&lt;ETX&gt;</b>
<b>IFL</b>	The controller is reversed to the function Local, if the Remote/Local switch is on the position Local. If the switch is on Remote position, it is not reversed. <b>Response: &lt;STX&gt;&lt;ACK&gt;&lt;ETX&gt;</b>
	<b>Directory of RAM</b>
<b>IPn</b>	Read out the n program <i>name</i> of the program list from the RAM. If no program name exists, the response NAK is shown (ONLY PC). <b>Response: &lt;STX&gt;&lt;ACK&gt;name&lt;ETX&gt;</b> <b>Response: &lt;STX&gt;&lt;NAK&gt;&lt;ETX&gt; no name available</b>
	<b>Information of the transmission protocol</b>
<b>ITR</b>	Read out the state of the RS interface transmission protocol
<b>ITSn</b>	Define the RS interface transmission protocol n=0 Instruction transmission <b>without</b> checksum n=1 Instruction transmission <b>with</b> checksum
	<b>Information operator panel</b>
<b>ITTSn</b>	Define type of operator panel n=0 drive without operator panel n=1 drive with operator panel BT5 n=2 drive with operator panel TP11
	<b>Version Request</b>
<b>IVR</b>	The software version of the controller is read (ONLY PC). <b>Response: &lt;STX&gt;&lt;ACK&gt;Software Version&lt;ETX&gt;</b>



## 2.9 Jump Instructions (ONLY PROG)

<u>Instruction</u>	<u>Meaning</u>
<b>Relative Jump</b>	
N+nn N–nn	Relative jump forward (+) or backward (–). Distance: nn program lines.
N+Rnn N–Rnn N+R[Rnn] N–R[Rnn]	Relative jump forward (+) or backward (–). Distance: number of program lines, defined by the content of register nn.
<b>Absolute Jump</b>	
Nnn	Absolute jump to program line number nn.
N*la*	Absolute jump. Destination program line number: marked by the label *la*.
NRnn NR[Rnn]	Absolute jump. Destination program line number: defined by the content of register nn.
NP[name]	Absolute jump to program <i>name</i> . Program starts at line number 1.
NP[name]Nnn	Absolute jump to program <i>name</i> . Program starts at line number nn.
NP[name]NRnn NP[name]NR[Rnn]	Absolute jump to program <i>name</i> . The program start line number is defined by the content of register nn.
NP[name]N*la*	Absolute jump to program <i>name</i> . The program start line is marked by the label *la*.
<b>Conditional Jump Relative / E = Condition Fulfilled</b>	
NE+nn NE–nn	Relative jump forward (+) or backward (–). Distance: nn program lines.
NE+Rnn NE–Rnn NE+R[Rnn] NE–R[Rnn]	Relative jump forward (+) or backward (–). Distance: number of program lines, defined by the content of register nn.
<b>Conditional Jump Absolute / E = Condition Fulfilled</b>	
NEnn	Absolute jump to program line number nn.
NE*la*	Absolute jump. Destination program line: marked by the label *la*.
NERnn NER[Rnn]	Absolute jump. Destination program line number: defined by the content of register nn.
NEP[name]	Absolute jump to program <i>name</i> . Program starts at line number 1.
NEP[name]Nnn	Absolute jump to program <i>name</i> . Program starts at line number nn.

## MINILOG

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<u>Instruction</u>	<u>Meaning</u>
<b>NEP[name]NRnn</b> <b>NEP[name]NR[Rnn]</b>	Absolute jump to program <i>name</i> . The program start line number is defined by the content of register nn.
<b>NEP[name]N*la*</b>	Absolute jump to program <i>name</i> . The program start line is marked by the label <i>*la*</i> .
<b>Conditional Jump Relative / N = Condition Not Fulfilled</b>	
<b>NN+nn</b> <b>NN–nn</b>	Relative jump forward (+) or backward (–). Distance: nn program lines.
<b>NN+Rnn</b> <b>NN–Rnn</b> <b>NN+R[Rnn]</b> <b>NN–R[Rnn]</b>	Relative jump forward (+) or backward (–). Distance: number of program lines, defined by the content of register nn.
<b>Conditional Jump Absolute / N = Condition Not Fulfilled</b>	
<b>NNnn</b>	Absolute jump to program line number nn.
<b>NN*la*</b>	Absolute jump. Destination program line number: marked by the label <i>*la*</i> .
<b>NNRnn</b> <b>NNR[Rnn]</b>	Absolute jump. Destination program line number: defined by the content of register nn.
<b>NNP[name]</b>	Absolute jump to program <i>name</i> . Program starts at line number 1.
<b>NNP[name]Nnn</b>	Absolute jump to program <i>name</i> . Program starts at line number nn.
<b>NNP[name]NRnn</b> <b>NNP[name]NR[Rnn]</b>	Absolute jump to program <i>name</i> . The program start line number is defined by the content of register nn.
<b>NNP[name]N*la*</b>	Absolute jump to program <i>name</i> . The program start line is marked by the label <i>*la*</i> .

## 2.10 Repeating of Program Lines

---

<u>Instruction</u>	<u>Meaning</u>
<b>NWnn</b>	The program line is nn times repeated.
<b>NWRnn</b> <b>NWR[Rnn]</b>	The program line is repeated as often as defined by the content of register nn.
	<b>Response: &lt;STX&gt;&lt;ACK&gt;&lt;ETX&gt; (ONLY PC)</b>

## 2.11 Passwort

### Instruction

### Meaning

**PA\_**

The controller is activated, if there is no password.  
Then it is not possible to lock the controller.

**PAname**

The password protected controller is activated.

**PSname**

This comand allocates the controller a password .  
It has maximum 8 alphanumeric signs.

### **Activation Status for Programs, Parameters and Registers**

**PWSp**

Set activation status

Programs, parameters and registers can be activated or locked by a password protected controller.

p → Activation status for programs, parameters and registers

p = 0 all activated

p = 1 Program R/W locked

p = 2 Parameter R/W locked

p = 4 Register R/W locked

p is between 0 and 7

Example: **PWS5**

Program and register locked (1+4=5)

**PWR**

Read activation status

The answer are two digits.

**<ACK> sp**

s □ Activation status of the controller

s = 0 Controller locked

s = 1 Controller activated

p → Activation status for programs, parameters and registers  
see above

Example: **PWR**

**<ACK> 15**

s = 1 → Controller activated

p = 5 → Program and register locked (1+4=5)

## 2.12 Ending or Interruption of a Program Call (ONLY PROG)

### Instruction

### Meaning

**PE**

The actual program is ended and the system waits for another changing of the **REMOTE/LOCAL** switch.

If the program was started via computer, the system goes back to the **COMPUTER MODE**.

## 2.13 Program and Data Management (ONLY PC)

---

<u>Instruction</u>	<u>Meaning</u>
	<b>Delete Programs and Data</b>
QDP*.*	All programs in the RAM are deleted
QDR	All registers in the RAM are set to zero.
	<b>Read Program Line</b>
QPname NnnR	The program line nn of the program <i>name</i> is read.
	<b>Program Start by line</b>
QPname NnnA	The program <i>name</i> is started from line nn.
	<b>Program Stop</b>
QPE	If the QPE Instruction is sent by the computer, it causes a jump back to the initial program level from which the program has been started.
	<b>Program Transmission with Read Out</b>
QPname Sbyte	The program <i>name</i> is to be transmitted block wise. During the transmission, the whole control sequence must be observed. The name must have 8 characters. The line number is not transmitted. name → maximal 8 characters, byte → number of bytes to be transmitted
	1. Computer: <b>&lt;STX&gt;controller address QPname Sbyte&lt;ETX&gt;</b> The program transmission sequence is started.
	2. Controller response: <b>STX&lt;ACK&gt;O&lt;ETX&gt;</b> if the program does not exist in the controller, and the controller's RAM capacity is sufficient.
	<b>&lt;STX&gt;&lt;ACK&gt;E&lt;ETX&gt;</b> If the program exists in the controller and must be overwritten.
	How to overwrite: 1) Backup of all programs of the MCC on the PC. 2) Delete all programs in the Flash RAM of the MCC. 3) Rewrite the programs (including revised prog.) to the MCC.

**Instruction**

**Meaning**

3. Program transmission:

- Start:

**<STX>controller address block 1<ETX>**

Block 1 is 256 byte long and starts with program name <ETB> .The program name must have 8 characters!

- Further blocks (block 1+x) always must have 256 bytes and are embedded in **<STX>controller address block 1+x<ETX**
- Last block:  
0x04 (EOT) must be the last character. If the last block is shorter than 256 bytes, the rest of the block must be filled with EOT.

Example:

**<STX>controller address block end <EOT><EOT>...  
<EOT><EOT><ETX>**

Controller response after each block:

**<STX><ACK><ETX>**

**Read Program with Request**

**QPname R**

The program *name* is to be read from the controller unit. The name must have 8 characters. The program is to be read by the line.

**Request and send**

1. Computer:

**<STX>controller address QPname R<ETX>**

The program *name* is to be read.

2. Controller response:

**<STX><ACK>Olnr<ETX>**

If the program is available, the controller unit reports the character O and the number of program lines lnr .

3. Computer:

**<STX>controller address J<ETX>**

The computer receives the first line of the controller.

4. Controller response:

**<STX>data program line x<ETX>**

The data are read line by line by indicating the line number.

Number 3. and 4. are repeated as long as all lines are received.

The transmission is finished by appending 0x04(EOT) to the last line.

Example for last line:

**<STX>Data last program line ...<EOT><EXT>**

## 2.14 Registers

---

- The MCC-2 controllers contain 1000 memory locations used to store variables, called **Registers** within MiniLog programs.
- The registers are numbered R1 to R1000.
- In each register numbers with up to ten digits can be entered. Decimal values are also programmable. Before and after the decimal point up to seven digits may be entered. The total number of digits must not exceed 8.
- If possible, the registers should be programmed in the first program lines.

Write value into a register: **RnnnnSzz**

Read value of a register: **RnnnnR**

Explanations:

<b>R</b>	instruction code: register
nnnn	register number
<b>S</b>	Write (Schreiben)
zz	number (maximum 10 digits)

- Within the program registers can be used for indirect input of positions. Combined with arithmetic calculations registers can be used as counters during program run.
- For all logic combinations or arithmetic calculations with registers please notice: The computed value will always be written into the first register named in the instruction.

Example: Add the values of two registers  
R18+R2 Value of register 2 is added to value of register 18.  
The result will be stored in register 18.

Compare register values

As the result of a comparison, a condition byte will be set by the program:

**E** = condition fulfilled,

**N** = condition not fulfilled.

The status of the condition byte can be used for a conditional jump, subroutine instructions or other operations.

Example: Comparison of a register value with a number and conditional jump  
R999=1 NE11 N77 If register 999 contains the value 1, jump to line 11, if not, jump to line 77.

## 2.15 Register Instructions

<u>Instruction</u>	<u>Meaning</u>
	<b>Register Value Integer</b>
Rnn.z	The digits after the decimal point of the register nn are deleted without truncation of the value. z = 0 – 6 digits after the decimal point
	<b>Set Outputs with Register Value</b>
RnnBAnn–mm R[Rnn]BAnn–mm	The content of the register nn is set as a binary value to the controller outputs nn to mm.
	<b>Load Register with Input Status</b>
RnnBEnn–mm R[Rnn]BEnn–mm	The status of the inputs nn to mm is written as a binary value into the register nn. <b>Example:</b> R1BE1–8 → Input status: <b>1010 0101</b> Result: <b>165</b>
	<b>Load Register with Hexadecimal Value</b>
RnnBSvalue R[Rnn]BSvalue	The register nn is set to the value. The data are fed in hexadecimal <b>Example:</b> R1BS1FA The register 1 is set to the hexadecimal value 1FA. After the instruction was carried out, the content of the register 1 is 506 decimal.
	<b>Shift Register Bit by Bit</b>
RnnBLm R[Rnn]BLm	The content of the register nn is shifted the number of m digits to the left (MSB ←). The right side of the register is filled in with zero. m = 1 to 27 → maximal value of the register content. <b>Example:</b> R1S168 R1BL2 The register 1 is set to the decimal value 168, corresponding to the binary value <b>10101000</b> . After the register content was shifted the number of 2 digits to the left, the binary value is <b>1010100000</b> which corresponds to the decimal value 672. <b>Response:</b> <STX><ACK><ETX> (ONLY PC)

# MINILOG

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## Instruction

## Meaning

RnnBRm  
R[Rnn]BRm

The content of the register nn is shifted the number of m digits to the right ( $\rightarrow$  LSB). The left side of the register is filled in with zero.

m = 1 to 27  $\rightarrow$  maximal value of the register content.

**Example:**     **R1S168 R1BR2**

The register 1 is set to the decimal value 168, corresponding to the binary value **10101000**. After the register content was shifted (R1BL2) the number of 2 digits to the right, the binary value is **101010** which corresponds to the decimal value 42.

### Register Bit Check

RnnBTm  
R[Rnn]BTm

The content of the register nn is regarded as a binary value. The digit in the position m of the binary value is checked. If the corresponding bit has been set, the condition byte is set. Otherwise the condition byte is reset.

m = 0 to 27  $\rightarrow$  maximal value of the register content.

**Example:**     **R1S168 R1BT4**

The register 1 is set to the decimal value 168, corresponding to the binary value **10101000**. The Instruction R1BT4 checks the 4<sup>th</sup> digit from the right side (m  $\leftarrow$  LSB) of the binary value. The condition byte is set, because the 4<sup>th</sup> digit has the value 1.

**Response:** <STX><ACK> E <ETX> or  
                  <STX><ACK> N <ETX> (ONLY PC)

### Logical Register Operations

#### Logic AND

RnnB^value  
R[Rnn]B^value

A logical **AND** operation is carried out with the content of the register nn and the hexadecimal value. The condition byte is set if the result is zero. Otherwise it is reset.

**Example:**     **R1BS2A8 R1B^1A0**

The register 1 is set to the hexadecimal value 2A8 (= 680 decimal). After the instruction **R1B^1A0** has been carried out, the content of the register 1 is 160 decimal.

	Decimal	Hex	Binary
	680	2A8	1010101000
	416	1A0	0110100000
<b>Result:</b>	160	0A0	0010100000

RnnB^Rmm  
R[Rnn]B^Rmm  
RnnB^R[Rmm]  
R[Rnn]B^R[Rmm]

A logical **AND** operation is carried out with the content of the register nn and the content of register mm. The condition byte is set if the result is zero. Otherwise it is reset.



**Instruction**

**Meaning**

**Logic OR**

RnnBvvalue  
R[Rnn]Bvvalue

A logical **OR** operation is carried out with the content of the register nn and the hexadecimal value. The condition byte is set if the result is zero. Otherwise it is reset.

**Example: R1BS2A8 R1Bv1A0**

The register 1 is set to the hexadecimal value 2A8 (= 680 decimal). After the instruction **R1Bv1A0** has been carried out, the content of the register 1 is 936 decimal.

	Decimal	Hex	Binary
	680	2A8	1010101000
	416	1A0	0110100000
<b>Result:</b>	<b>936</b>	<b>3A8</b>	<b>1110101000</b>

RnnBvRmm  
R[Rnn]BvRmm  
RnnBvR[Rmm]  
R[Rnn]BvR[Rmm]

A logical **OR** operation is carried out with the content of the register nn and the content of register mm. The condition byte is set if the result is zero. Otherwise it is reset.

**Response: <STX><ACK><ETX> (ONLY PC)**

**Logical Exclusive OR**

RnnBXvalue  
R[Rnn]BXvalue

A logical **XOR** operation is carried out with the content of the register nn and the hexadecimal value. The condition byte is set if the result is zero. Otherwise it is reset.

**Example: R1BS2A8 R1BX1A0**

The register 1 is set to the hexadecimal value 2A8 (= 680 decimal). After the instruction **R1BX1A0** has been carried out, the content of the register 1 is 776 decimal.

	Decimal	Hex	Binary
	680	2A8	1010101000
	416	1A0	0110100000
<b>Result:</b>	<b>776</b>	<b>308</b>	<b>1100001000</b>

RnnBXRmm  
R[Rnn]BXRmm  
RnnBXR[Rmm]  
R[Rnn]BXR[Rmm]

A logical **XOR** operation is carried out with the content of the register nn and the content of register mm. The condition byte is set if the result is zero. Otherwise it is reset.

**Compare Register Content with Number Values**

Rnn=value  
R[Rnn]=value

The content of register nn is compared with a number (value). The condition byte is set if equality has been detected. Otherwise it is reset.

Rnn#value  
R[Rnn]#value

The content of register nn is compared with a number (value). The condition byte is set if inequality has been detected. Otherwise it is reset.

## MINILOG

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### Instruction

### Meaning

Rnn>value  
R[Rnn]>value

The content of register nn is compared with a number (value). The condition byte is set if the register value is higher. Otherwise it is reset.

Rnn<value  
R[Rnn]<value

The content of register nn is compared with a number (value). The condition byte is set if the register value is lower. Otherwise it is reset.

### **Compare Register Content**

Rnn=Rmm  
R[Rnn]=Rmm  
Rnn=R[Rmm]  
R[Rnn]=R[Rmm]

The content of register nn is compared with the content of register mm. The condition byte is set if equality has been detected. Otherwise it is reset.

Rnn#Rmm  
R[Rnn]#Rmm  
Rnn#R[Rmm]  
R[Rnn]#R[Rmm]

The content of register nn is compared with the content of register mm. The condition byte is set if inequality has been detected. Otherwise it is reset.

Rnn>Rmm  
R[Rnn]>Rmm  
Rnn>R[Rmm]  
R[Rnn]>R[Rmm]

The content of register nn is compared with the content of register mm. The condition byte is set if the value of register nn is higher. Otherwise it is reset.

Rnn<Rmm  
R[Rnn]<Rmm  
Rnn<R[Rmm]  
R[Rnn]<R[Rmm]

The content of register nn is compared with the content of register mm. The condition byte is set if the value of register nn is lower. Otherwise it is reset.

### **Response for all relations:**

<STX><ACK> E <ETX> or  
<STX><ACK> N <ETX> (ONLY PC)

### **Arithmetical Register Operations**

#### **Addition**

Rnn+value  
R[Rnn]+value  
Rnn+Rmm  
Rnn+R[Rmm]  
R[Rnn]+Rmm  
R[Rnn]+R[Rmm]

The value is added to the content of register nn.

The content of register mm is added to the content of register nn.

#### **Subtraction**

Rnn-value  
R[Rnn]-value  
Rnn-Rmm  
Rnn-R[Rmm]  
R[Rnn]-Rmm  
R[Rnn]-R[Rmm]

The value is subtracted from the content of the register nn.

The content of register mm is subtracted from the content of the register nn.

#### **Multiplication**

Rnn\*value  
R[Rnn]\*value

The content of register nn is multiplied by the value.

<b><u>Instruction</u></b>	<b><u>Meaning</u></b>
Rnn*Rmm R[Rnn]*Rmm Rnn*R[Rmm] R[Rnn]*R[Rmm]	The content of the register nn is multiplied by the content of register mm.
<b>Division</b>	
Rnn:value R[Rnn]:value Rnn/value R[Rnn]/value Rnn:Rmm Rnn:R[Rmm] R[Rnn]:Rmm R[Rnn]:R[Rmm]	The content of register nn is divided by the value.  The content of register nn is divided by the content of register mm.  The content of register nn is divided by the value.
Rnn/Rmm Rnn/R[Rmm] R[Rnn]/Rmm R[Rnn]/R[Rmm]	The content of register nn is divided by the content of register mm.
<b>Trigonometric Functions</b>	
RnnSIN RnnCOS RnnTAN	Sinus, Cosinus or Tangent is evaluated from the value of the register nn and the result is written back to the register nn.
<b>Square Root</b>	
RnnQW	The square root is evaluated from the value of the register nn and written back to the register nn.
<b>Random Number</b>	
RnnRAND	The register nn is set with the random number in the range 0 to 4294967296 ( $2^{32}$ ).
<b>Read Register</b>	
RnnR R[Rnn]R	The content of register nn is read (ONLY PROG). <b>Response: &lt;STX&gt;&lt;ACK&gt;value&lt;ETX&gt;</b> <b>Response for all arithmetical operations:</b> <b>&lt;STX&gt;&lt;ACK&gt;&lt;ETX&gt; (ONLY PC)</b>
<b>Write Register</b>	
<b>with Decimal Values:</b>	
RnnSvalue R[Rnn]Svalue	Register nn is set to the value.
<b>with Register Values:</b>	
RnnSRmm R[Rnn]SRmm	Register nn is set to the value of register mm.

## MINILOG

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### Instruction

### Meaning

RnnSR[Rmm]  
R[Rnn]SR[mm]

#### with Parameter Values

RnnSXPmm  
R[Rnn]SXPmm

Register nn is set to the parameter mm of axis x.

#### with line number emergency stop

RnnSN  
R[Rnn]SN

The register nn is set with the line number, in which an emergency stop started.

**Example :**    Lnr 001 FN10  
                  Lnr 002 X+1000  
                  Lnr 003 X-1000 H N2  
                  Lnr 010 R1SN

In this example an emergency stop program is defined in line 10. The x-axis drives 1000 steps in + and – direction. In case of an emergency stop during an axis drive, the program continues in line 10 and the axis is stopped. With the instruction **R1SN** the register 1 is set with the line number, in which the emergency stop started. Then, it is possible to interpret at which positioning the emergency stop started.

#### with the Timer Ticker Value

RnnSTT

The register nn is written with the timer ticker value.

#### with the Program Line Number

RnnSZ

The program line number at which this instruction is called is written into the register nn.

R[Rnn]SZ

The program line number at which this instruction is called is written into the register which is addressed by the register nn.

**Example :**    Lnr 041 R1S10  
                  Lnr 042 R[R1]SZ

In this example the register 1 is written with the value 10. The register 10 is written with the instruction **R[R1]SZ** by the actual line number. The register content 10 is now 42. This instruction can be used for automatic start functions.

#### Write Register via Inputs

RnnSEmm-xx.k  
R[Rnn]SEmm-xx.k

A BCD value is written via the inputs mm to xx into the register nn.  
k = number of digits after the decimal point.

**Example: R1SE1-8.1**

The inputs 1 to 8 have e.g. the status: **1001 0011**. The result is 9.3.

<u><b>Instruction</b></u>	<u><b>Meaning</b></u>
	<b>Change Register with Terminal</b>
<b>RnnST</b>	<p>Register nn is displayed in line 4 from position 10. New data are input at the cursor position. The register nn is rewritten by pressing the ENTER key of the terminal.</p> <p><b>Example :</b>     <b>R41ST</b></p> <p>In this example the register 41 is displayed in the 4th line of the terminal display from the 10th position and is ready for editing.</p> <p><b>Response: &lt;STX&gt;&lt;ACK&gt;&lt;ETX&gt; if terminal available</b>  <b>&lt;STX&gt;&lt;NAK&gt;&lt;ETX&gt; if no terminal available</b>            (ONLY PC)</p>
<b>RnnST.z</b>	<p>Register nn is displayed in line 4 with z digits after the decimal point (z=0 to 6). New data are input at the cursor position. The register nn is rewritten by pressing the ENTER key of the terminal.</p> <p><b>Example :</b>     <b>R2ST.6</b></p> <p>The register 2 is displayed with 6 digits after the decimal point on the terminal and is rewritten.</p>
<b>RnnSTy</b>	<p>Register nn in line y is displayed (y=1 to 4) and is rewritten after new input with ENTER key.</p> <p><b>Example :</b>     <b>R2ST3</b></p> <p>The register 2 is displayed in the 3rd line on the terminal from position 1 and is rewritten.</p>
<b>RnnSTy.z</b>	<p>Register nn in line y (y=1 to 4) is displayed with z digits after the decimal point (z=0 to 6) and is rewritten after new input with ENTER key.</p> <p><b>Example :</b>     <b>R2ST3.4</b></p> <p>The register 2 is displayed in the 3rd line with 4 digits after the decimal point from position 1 on the terminal and is rewritten.</p>
<b>RnnSTy;m</b>	<p>Register nn in line y (y=1 to 4) is displayed from position m (m=1 to 20) and is rewritten after new input with ENTER key.</p> <p><b>Example :</b>     <b>R1ST3;6</b></p> <p>The register 1 is displayed in the 3rd line from position 6 and is newly written.</p>
<b>RnnSTy;m.z</b>	<p>Register nn in line y (y=1 to 4) is displayed with z digits after the decimal point (z=0 to 6) from position m (m=1 to 20) and is rewritten after new input with ENTER key.</p> <p><b>Example :</b>     <b>R2ST2;2.6</b></p> <p>The register 2 is displayed in the 2nd line from position 2 with 6 digits after the decimal point from position 1 on the terminal and is rewritten.</p>

## MINILOG

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### Instruction

### Meaning

#### with A/D converter values

RnnSADy  
R[Rnn]SADy

Register nn is written by the A/D converter value.  
y=1 to 2: A/D converter channel

#### Display Register with Terminal

RnnWy

The value of register nn is displayed in line y from position 1 (y=1 to 4).

#### Example : R2W2

Register 2 is displayed in line 2 from position 1.

RnnWy.z

The value of register nn is displayed in line y from position 1 (y=1 to 4) with z digits after the decimal point (z=0 to 6).

#### Example : R1W4.6

The register 1 is displayed in the 4th line from the 1st position with 6 digits after the decimal point.

RnnWy;m

The value of register nn is displayed in line y from position m (y=1 to 4, m= 1 to 20).

#### Example : R2W3;5

The register 2 is displayed in the 3rd line from the 5th position.

RnnWy;m.z

The value of register nn is displayed in line y from position m (y=1 to 4, m= 1 to 20, z=0 to 6) with z digits after the decimal point.

#### Example : R7W2;5;3

The register 7 is displayed in the 2nd line from the 5th position with 3 digits after the decimal point.

**Answer:** <STX><ACK><ETX> (ONLY PC)

## 2.16 System Status (ONLY PC)

<u>Instruction</u>	<u>Meaning</u>
	<b>System Status General</b>
<b>S</b>	<p>Axes check and request for the number of axes.</p> <p><b>Response:</b>&lt;STX&gt;&lt;ACK&gt;n IO &lt;ETX&gt;</p> <p>n = number of axes</p>
	<b>System Status Binary</b>
<b>SB</b>	<p>Read system status in binary format (<math>d_B = 0</math> or 1).</p> <p><b>Response:</b> &lt;STX&gt;&lt;ACK&gt;<math>d_{B8}</math>..... <math>d_{B1}</math>&lt;ETX&gt;</p> <p><math>d_B 1 = 1 \rightarrow</math> Program Run  <math>d_B 2 = 1 \rightarrow</math> Software Remote  <math>d_B 3 = 1 \rightarrow</math> Emergency limit switch of an axis  <math>d_B 4 = 1 \rightarrow</math> Power stage failure of an axis  <math>d_B 5 = 1 \rightarrow</math> Error programming (reset after status request)  <math>d_B 6 = 1 \rightarrow</math> Terminal is activated  <math>d_B 7 = 1 \rightarrow</math> SRQ has been set  <math>d_B 8 = 1 \rightarrow</math> Computer call</p>
	<b>System Status Extended</b>
<b>SE</b>	<p>Read system status in hexadecimal code. Two bytes (4 hexadecimal digits <math>d_H</math>) are available per axis: 1. + 2. byte for the x-axis, 3. + 4. byte for the y-axis.</p> <p><b>Response:</b>&lt;STX&gt;&lt;ACK&gt;<math>d_{HX}d_{HX}d_{HX}d_{HX}d_{HY}d_{HY}d_{HY}d_{HY}</math>&lt;ETX&gt;</p> <p>Bit 0 = 1 <math>\rightarrow</math> Power stage overcurrent            Bit 1 = 1 <math>\rightarrow</math> Power stage under voltage            Bit 2 = 1 <math>\rightarrow</math> Power stage overtemperature            Bit 3 = 1 <math>\rightarrow</math> Power stage is activated            Bit 4 = 1 <math>\rightarrow</math> Initiator – is activated (emergency stop)            Bit 5 = 1 <math>\rightarrow</math> Initiator + is activated            Bit 6 = 1 <math>\rightarrow</math> Step failure (only with option SFI = Step Failure Indication)            Bit 7 = 1 <math>\rightarrow</math> Encoder error            Bit 8 = 1 <math>\rightarrow</math> Motor stands still            Bit 9 = 1 <math>\rightarrow</math> Reference point is driven and OK (is reset at stop by initiator)</p> <p>(Bit 10 to Bit 15 not reserved)</p> <p>If Bit 0 to Bit 2 are set at the same time, no power stage is connected. Otherwise, only one error is possible at the time.</p>

# MINILOG

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## Instruction

## Meaning

**SH**

### **System Status Axes**

Axis test with status axes output.

**Response:**<STX><ACK> E <ETX>, if all axes are stopped.

<STX><ACK> N <ETX>, if any axis is running.

### **System Status Decimal**

**ST**

Read system status as decimal number.

**Response:** <STX><ACK>value <ETX>

value = number between 0 and 255

0 = End of program in the LOCAL MODE

1 = Program run

2 = Software Remote

4 = Emergency limit switch of an axis

8 = Power stage failure of an axis

16 = Error programming (reset after status request)

32 = Terminal or Enable is activated

64 = SRQ has been set

128 = Computer Mode

### **Initiators**

**SUI**

Read status of initiators (limit switches).

**Response:**<STX><ACK>I=n <ETX>

n = 0 → Axis is free, no initiator has reacted

n = + → Initiator + direction has reacted

n = – → Initiator – direction has reacted

n = 2 → Both initiators have reacted (that means: wrong polarity of the initiators, broken wire or no 24 V supply voltage)

### **Synchronous Start**

**S1**

Prepare the synchronous start of the axes

**S0**

Execute the synchronous start of the axes.



## 2.17 Store Data into Flash EPROM

<u>Instruction</u>	<u>Meaning</u>
	<b>Store programs and axis parameters (ONLY PC)</b>
SA	Axis parameters are stored into the EPROM.

## 2.18 Time Loops

<u>Instruction</u>	<u>Meaning</u>
Tvalue TRnn TR[Rnn]	The value for time loops (value, content of register nn or register [Rnn]) is preset in ms. The program waits here until the preset time has run out. <b>Response: &lt;STX&gt;&lt;ACK&gt;&lt;ETX&gt; (ONLY PC)</b>
TTSvalue TTSRnn TTSR[Rnn]	The timer is loaded with a time (ms) value (value, content of register nn or register [Rnn]). The timer counts down to zero. The program is not interrupted. <b>Response: &lt;STX&gt;&lt;ACK&gt;&lt;ETX&gt; (ONLY PC)</b>
TT=0	The timer is compared with zero. If the timer is equal to zero the condition byte is set. Otherwise it is reset. Timer = 0 : the preset time is passed.
TT>value TT>Rnn TT>R[Rnn] TT<value TT<Rnn TT<R[Rnn]	The timer is compared with the preset value (value, content of register nn or register [Rnn]). If the timer value is higher/lower than the preset value (condition fulfilled) the condition byte is set. Otherwise it is reset. <b>Response: &lt;STX&gt;&lt;ACK&gt;E&lt;ETX&gt; or &lt;STX&gt;&lt;ACK&gt;N&lt;ETX&gt; (ONLY PC)</b>

## 2.19 Subroutines (ONLY PROG)

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<u>Instruction</u>	<u>Meaning</u>
	<b>Break Off Subroutine</b>
<b>UA</b>	Break off all subroutines and set stack. The program can be continued with a jump instruction.
	<b>End of Subroutine</b>
<b>UE</b>	The subroutine is finished and the program is continued at the program line where this subroutine has been called.
	<b>Call of Subroutine</b>
<b>U<sub>nn</sub></b>	The subroutine with the start line <i>nn</i> is called. The subroutine can be ended by means of the instruction UE.
<b>UR<sub>nn</sub></b> <b>UR[R<sub>nn</sub>]</b>	The register <i>nn</i> or [R <sub>nn</sub> ] contains the start line of the called subroutine. The subroutine is ended with the instruction UE.
<b>U*<i>la</i>*</b>	The subroutine starts at that line which is indicated by the label * <i>la</i> *. The subroutine is ended by the instruction UE.
<b>UP[<i>name</i>]</b>	The subroutine <i>name</i> (start line number 1) is called. The subroutine is ended by the instruction UE.
<b>UP[<i>name</i>]<b>N</b><sub>nn</sub></b>	The subroutine <i>name</i> (start line number <i>nn</i> ) is called. The subroutine is ended by the instruction UE.
<b>UP[<i>name</i>]<b>NR</b><sub>nn</sub></b> <b>UP[<i>name</i>]<b>NR</b>[R<sub>nn</sub>]</b>	The subroutine <i>name</i> starts at that program line which is stored in the register <i>nn</i> or [R <sub>nn</sub> ]. The subroutine is ended by the instruction UE.
<b>UP[<i>name</i>]<b>N</b>*<i>la</i>*</b>	The subroutine <i>name</i> starts at that line which is indicated by the label * <i>la</i> *. The subroutine is ended by the instruction UE.
	<b>Conditional Subroutine Call</b>
	All instruction variants described above are available for the conditional subroutine call. The instruction call is only completed by the letter „E“ for condition fulfilled or „N“ for condition not fulfilled.
	<b>"E" = Condition fulfilled</b>
<b>U<sub>enn</sub></b>	see <b>U<sub>nn</sub></b> , page 32
<b>UER<sub>nn</sub></b> <b>UE[R<sub>nn</sub>]</b>	
<b>UE*<i>la</i>*</b>	see <b>U*<i>la</i>*</b> , page 32
<b>UEP[<i>name</i>]</b>	see <b>UP[<i>name</i>]</b> , page 32
<b>UEP[<i>name</i>]<b>N</b><sub>nn</sub></b>	

<u>Instruction</u>	<u>Meaning</u>
UEP[name]NRnn UEP[name]NR[Rnn ]	
UEP[name]N*la*	see UP[name]N*la*, page 32
	<b>"N" = Condition not fulfilled</b>
Unnn	see Unn, page 32
UNRnn UNR[Rnn]	
UN*la*	see U*la*, page 32
UNP[name]	see UP[name], page 32
UNP[name]Nnn UNP[name]NRnn UNP[name]NR[Rnn ]	
UNP[name]N*la*	see UP[name]N*la*, page 32

## 2.20 Terminal Instructions (also by PC in case of terminal connection)

<u>Instruction</u>	<u>Meaning</u>
<>Wy	Erase text on line y (y=1 to 4)
<text>Wy	Display the text in line y from position 1 (y=1 to 4)
<text>Wy;m	Display the text in line y from position m (y=1 to 4; M=1 to 20)
	<b>Response: &lt;STX&gt;&lt;ACK&gt;&lt;ETX&gt;</b>

2.21 Axes Instructions

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<u>Instruction</u>	<u>Meaning</u>
XC	Reset x-axis
YC	Reset y-axis
	<b>Response:</b> <STX><ACK><ETX> (ONLY PC)
	<b>Axis Status Request</b>
X=E	Axis request on power stage error.
X#E	Check (=) if a power stage error has occurred or check (#) if the power stage is operating normally. The error message "Failure" is requested.
X=H	Axis request on stillstand.
X#H	Check (=) if the axis is in standstill or check (#) if the axis is in motion. The condition byte is set when the condition is fulfilled. Otherwise it is reset.
X=I+	Axis request on initiator status.
X=I-	The condition byte is set when the axis has come to a standstill at the initiator or the initiator is not connected. Otherwise it is reset.
X=M	Axis request on power stage error.
X#M	Check power stage (=), if a <b>Step failure</b> has occurred or has not (#) occurred.  The condition byte is set, when the condition is fulfilled. Otherwise it is reset.  This instruction applies only to control units with optional <b>Step Failure Indication (SFI)</b> board.
X=N	Axis request on emergency stop.
X#N	Check (=) if the axis has come to a standstill (or not (#)) at an emergency switch.  The condition byte is set when the condition is fulfilled. Otherwise it is reset.
	<b>Response:</b> <STX><ACK>E<ETX> or <STX><ACK>N<ETX> (ONLY PC)
	<b>Wait until Set Point is reached</b>
X>value	The axis X is positioned and the program waits until the value of the counter XP21 is higher than the preset value (value, content of register nn or register [Rnn]). If the XP21 value is higher or the axis has come to a standstill the program is continued.
X>Rnn	
X>R[Rnn]	
<b>Example:</b>	<b>Inr 005 XP21S0 XP14S2000 XL+</b> <b>Inr 006 X&gt;5000 XP14S1000</b> <b>Inr 007 X&gt;10000 XS XP14S2000</b>

<b><u>Instruction</u></b>	<b><u>Meaning</u></b>
	<p>The axis is to be moved 10000 steps with 2000 Hz. After 5000 steps, the frequency is lowered to 1000 Hz and is set to 2000 Hz again after the standstill of the axis. At the instruction <b>X&gt;5000</b> the program is stopped and will be continued after the position 5000 is reached or the axis has been stopped by an emergency stop.</p>
<b>X&lt;value</b> <b>X&lt;Rnn</b> <b>X&lt;R[Rnn]</b>	<p>The axis x is positioned and the program waits until the value of the counter xP21 is lower than the preset value (value, content of register nn or register [Rnn]). If the xP21 value is lower or the axis has come to a standstill the program is continued.</p> <p><b>Response:</b> &lt;STX&gt;&lt;ACK&gt;&lt;ETX&gt; (ONLY PC), if the axis has come to a standstill or the position condition is fulfilled.</p> <p>Otherwise the program waits.</p>
	<p><b>Switching Power Stages</b></p>
	<p><b>Activate</b></p>
<b>XMA</b>	The power stage of axis X is activated.
	<p><b>Deactivate</b></p>
<b>XMD</b>	The power stage of axis X is deactivated.
	<p><b>Axis parameter</b></p>
<b>XPmmR</b>	The parameter mm of axis x is read out. (Only PROG)
	<p><b>Response :</b> &lt;STX&gt;&lt;ACK&gt;value&lt;ETX&gt;  mm = Parameter ID (ONLY PROG)</p>
<b>XPmmSvalue</b> <b>XPmmSRnn</b> <b>XPmmSR[Rnn]</b>	<p>The parameter mm of axis x is loaded with the preset value (value, the content of register nn or register [Rnn]).  mm = Parameter ID</p>
	<p><b>Initialisation/Reference Search Run</b></p>
	<p>To initialize an axis, a reference search run has to be carried out. The initiators, also called limit switches, serve as reference point. The axis moves to an initiator. When the initiator signal is identified, the motor stops and moves as long in the opposite direction until there is no more initiator signal. In case of initiator offset setting the offset distance is run and the axis is stopped. This point is called MØP (mechanical zero point) or reference point.</p>
<b>X0-</b>	The axis moves to the initiator of the – direction.
<b>X0+</b>	The axis moves to the initiator of the + direction.
<b>X0-I</b>	The axis moves in – direction and stops with the zero pulse of the incremental encoder. Only incremental, no SSI Encoder!

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<u>Instruction</u>	<u>Meaning</u>
<b>X0+I</b>	The axis moves in + direction and stops with the zero pulse of the incremental encoder. Only incremental, no SSI Encoder! <b>Response: &lt;STX&gt;&lt;ACK&gt;&lt;ETX&gt; (ONLY PC)</b>
<b>X0-^I</b>	The axis moves to the initiator of the – direction. After the offset distance the axis moves again until the zero impulse of the Incremental encoder stops the axis. Only incremental, no SSI Encoder!
<b>X0 +^I</b>	The axis moves to the initiator of the + direction. After the offset distance the axis moves again until the zero impulse of the Incremental encoder stops the axis. Only incremental, no SSI Encoder!
	<b>Free Running</b>
<b>XLr</b>	The axis is started and runs as long as it is stopped by the instruction xS or by a limit switch. r = + or – running direction
	<b>Relative Positioning</b>
<b>Xrvalue</b> <b>XrRnn</b> <b>XrR[Rnn]</b>	The axis runs the distance relatively which is preset by value, the content of register Rnn or register [Rnn]. r = + or – running direction
	<b>with stop instruction via input</b>
<b>XrvaluevEnnz</b> <b>XrRnnvEnnz</b> <b>XrR[Rnn]vEnnz</b>	The axis runs relatively with its creep speed the distance which is preset by value, the content of Rnn or register [Rnn]. The axis stops prematurely if the input nn gets the status z or a limit switch stops the positioning. r = + or – running direction z = S → input set z = R → input reset
<b>XrvaluevvEnnz</b> <b>XrRnnvvEnnz</b> <b>XrR[Rnn]vvEnnz</b>	The axis runs relatively with its high speed the distance which is preset by value, the content of Rnn or register [Rnn]. The axis stops prematurely if the input nn gets the status z or a limit switch stops the positioning. r = + or – running direction z = S → input set z = R → input reset

**Instruction**

**Meaning**

**Absolute Positioning Related to the MØP**

**XArvalue**  
**XArRnn**  
**XArR[Rnn]**

The axis runs, in relation to the mechanical zero point MØP (XP20) to the absolute position, which is preset by value, the content of Rnn or register [Rnn].

r = + or – running direction

**with stop instruction via input**

**XArvaluevvEnnz**  
**XArRnnvvEnnz**  
**XArR[Rnn]vvEnnz**

The axis runs with high speed, in relation to the mechanical zero point MØP to the absolute position, which is preset by value, the content of Rnn or register [Rnn]. The axis stops prematurely if the input nn gets the status z or a limit switch stops axis run.

r = + or – running direction  
z = S → input set  
z = R → input reset

**Absolute Positioning Related to the ELØP**

**Xervalue**  
**XErRnn**  
**XErR[Rnn]**

The axis runs, in relation to the electrical zero point (ELØP ) to the absolute position, which is preset by value, the content of Rnn or register [Rnn].

r = + or– running direction  
z = S → input set  
z = R → input reset

**With stop instruction via input**

**XErvaluevvEnnz**  
**XErRnnvvEnnz**  
**XErR[Rnn]vvEnnz**

The axis runs with high speed, in relation to the electrical zero point (ELØP) to the absolute position, which is preset by value, the content of Rnn or register [Rnn]. The axis stops prematurely if the input nn gets the status z or a limit switch stops axis run.

r = + or – running direction  
z = S → input set  
z = R → input reset

**Axis Stop**

**XS**  
**XSN**

All running instructions are cut off. The axis stops with the preset ramp.

The axis stopps with the preset emergency stop ramp (parameter P7).

2.22 Function Keys Read Out on Terminal BT24 (also by PC)

---

<u>Instruction</u>	<u>Meaning</u>
	<b>Conditional Keyboard Read Out</b>
<b>#vFn</b>	If the function key n is being depressed, the conditional byte is set. Otherwise it is reset.  n = function key F1 to F6
<b>#vnmX</b>	If the key n or m or x is being depressed, the conditional byte is set. Otherwise it is reset.  n, m, x = 0 to 9 (key 0 to 9) n, m, x = L (key CURSOR LEFT) n, m, x = R (key CURSOR RIGHT) n, m, x = U (key CURSOR UP) n, m, x = D (key CURSOR DOWN) n, m, x = H (key CURSOR HOME) n, m, x = B (key SCROLL) n, m, x = C (key CLEAR) n, m, x = E (key ENTER) n, m, x = P (key PRINT) n, m, x = ? (key ?) n, m, x = + (key +) n, m, x = - (key -) n, m, x = . (key .)

**Example: ZNR 005 #vH1? NN-0**

The BT24 keyboard is scanned as long as the key **H, 1** or **?** is being depressed. The conditional byte is reset, if the keys **HOME, 1** or **?** are not depressed. By the instruction **NN-0** the program jumps to the line hold of line 5.

**Important:** The key ENTER is not defined for a read out.

**Response:** <STX><ACK> E <ETX> or  
<STX><ACK> N <ETX> (ONLY PC)



### 3 List of Minilog Instructions

#vFn .....	38	N*la* .....	15
#vnmX.....	38	N+nn .....	15
<>Wy .....	33	N+R[Rnn] .....	15
<text>Wy .....	33	N+Rnn .....	15
ADnR.....	10	NE*la* .....	15
AGnR.....	9	NE+nn.....	15
AGnSZZZZZZZ.....	9	NE+R[Rnn] .....	15
Annnz .....	9	NE+Rnn .....	15
Annnzmmmzxxxz .....	9	NEnn.....	15
ARnnn;mmm;xxx .....	9	NE-nn.....	15
CR .....	10	NEP[name] .....	15
CT.....	10	NEP[name]N*la* .....	16
D1 .....	10	NEP[name]Nnn.....	15
D2.....	10	NEP[name]NR[Rnn].....	16
D3.....	10	NEP[name]NRnn .....	16
E^nnzmmzxxxz .....	11	NER[Rnn] .....	15
EASnnnnnnnn .....	12	NE-R[Rnn] .....	15
EGnR.....	12	NERnn .....	15
Ennz .....	11	NE-Rnn .....	15
Ennzmmz.....	12	NN*la* .....	16
ERnn;mm;xx .....	12	NN+nn .....	16
Evnnzmmzxx .....	11	NN+R[Rnn] .....	16
FN*la* .....	13	NN+Rnn.....	16
FNznr.....	13	Nnn .....	15
FP[name].....	13	N-nn .....	15
H .....	13	NNnn .....	16
IAR .....	13	NN-nn.....	16
IBR .....	13	NNP[name].....	16
IBSname.....	13	NNP[name]N*la* .....	16
ICnR .....	13	NNP[name]Nnn .....	16
ICnSbaud.....	13	NNP[name]NR[Rnn].....	16
IFL .....	14	NNP[name]NRnn.....	16
IFR.....	14	NNR[Rnn] .....	16
IPn .....	14	NN-R[Rnn] .....	16
ITR.....	14	NN-Rnn .....	16
ITSn.....	14	NNRnn.....	16
ITTSn.....	14	NP[name] .....	15
IVR .....	14	NP[name]N*la* .....	15
<text>Wy .....	33	NP[name]Nnn .....	15

# MINILOG

NP[name]NR[Rnn] .....	15	R[Rnn]=value.....	23
NP[name]NRnn .....	15	R[Rnn]>R[Rmm].....	24
NR[Rnn] .....	15	R[Rnn]>Rmm.....	24
N-R[Rnn] .....	15	R[Rnn]>value.....	24
NRnn .....	15	R[Rnn]B^R[Rmm] .....	22
N-Rnn .....	15	R[Rnn]B^Rmm .....	22
NWnn.....	16	R[Rnn]B^value .....	22
NWR[Rnn] .....	16	R[Rnn]BA <sub>nn-mm</sub> .....	21
NWRnn .....	16	R[Rnn]BE <sub>nn-mm</sub> .....	21
PAname.....	17	R[Rnn]BL <sub>m</sub> .....	21
PA_.....	17	R[Rnn]BR <sub>m</sub> .....	22
PE.....	17	R[Rnn]BSvalue.....	21
PSname.....	17	R[Rnn]BT <sub>m</sub> .....	22
PWR .....	17	R[Rnn]BvR[Rmm] .....	23
PWSp .....	17	R[Rnn]BvRmm .....	23
QDP*.* .....	18	R[Rnn]Bvvalue .....	23
QDR.....	18	R[Rnn]BXR[Rmm].....	23
QPE.....	18	R[Rnn]BXRmm.....	23
QPname NnnA .....	18	R[Rnn]BXvalue.....	23
QPname NnnR .....	18	R[Rnn]R.....	25
QPname R .....	19	R[Rnn]-R[Rmm].....	24
QPname Sbyte .....	18	R[Rnn]-Rmm.....	24
R[Rnn]:R[Rmm] .....	25	R[Rnn]SADy.....	28
R[Rnn]#R[Rmm].....	24	R[Rnn]SE <sub>mm-xx.k</sub> .....	26
R[Rnn]:Rmm .....	25	R[Rnn]SN .....	26
R[Rnn]#Rmm.....	24	R[Rnn]SR[mm] .....	25
R[Rnn]:value .....	25	R[Rnn]SRmm .....	25
R[Rnn]#value.....	23	R[Rnn]Svalue .....	25
R[Rnn]*R[Rmm] .....	25	R[Rnn]SXPmm .....	26
R[Rnn]*Rmm .....	25	R[Rnn]SZ.....	26
R[Rnn]*value .....	24	R[Rnn]-value.....	24
R[Rnn]/R[Rmm].....	25	Rnn:R[Rmm] .....	25
R[Rnn]/Rmm.....	25	Rnn#R[Rmm].....	24
R[Rnn]/value.....	25	Rnn:Rmm .....	25
R[Rnn]+R[Rmm].....	24	Rnn#Rmm.....	24
R[Rnn]+Rmm .....	24	Rnn:value .....	25
R[Rnn]+value .....	24	Rnn#value.....	23
R[Rnn]<R[Rmm].....	24	Rnn*R[Rmm] .....	25
R[Rnn]<Rmm .....	24	Rnn*Rmm .....	25
R[Rnn]<value .....	24	Rnn*value .....	24
R[Rnn]=R[Rmm].....	24	Rnn.z .....	21
R[Rnn]=Rmm .....	24	Rnn/R[Rmm].....	25

Rnn/Rmm .....	25	RnnST.....	27
Rnn/value .....	25	RnnST.z.....	27
Rnn+R[Rmm] .....	24	RnnSTT .....	26
Rnn+Rmm .....	24	RnnSTy.....	27
Rnn+value .....	24	RnnSTy.z.....	27
Rnn<R[Rmm] .....	24	RnnSTy;m.....	27
Rnn<Rmm .....	24	RnnSTy;m.z.....	27
Rnn<value .....	24	RnnSvalue .....	25
Rnn=R[Rmm] .....	24	RnnSXPmm .....	26
Rnn=Rmm .....	24	RnnSZ.....	26
Rnn=value .....	23	RnnTAN.....	25
Rnn>R[Rmm] .....	24	Rnn-value.....	24
Rnn>Rmm .....	24	RnnWy.....	28
Rnn>value .....	24	RnnWy.z .....	28
RnnB^R[Rmm].....	22	RnnWy;m.....	28
RnnB^Rmm.....	22	RnnWy;m.z .....	28
RnnB^value.....	22	<b>S</b> .....	29
RnnBAnn-mm.....	21	<b>S0</b> .....	30
RnnBEnn-mm.....	21	<b>S1</b> .....	30
RnnBLm.....	21	<b>SA</b> .....	31
RnnBRm.....	22	<b>SB</b> .....	29
RnnBSvalue .....	21	<b>SE</b> .....	29
RnnBTm .....	22	<b>SH</b> .....	30
RnnBvR[Rmm].....	23	<b>ST</b> .....	30
RnnBvRmm.....	23	<b>SUI</b> .....	30
RnnBvvalue.....	23	<b>TR[Rnn]</b> .....	31
RnnBXR[Rmm].....	23	<b>TRnn</b> .....	31
RnnBXRmm .....	23	<b>TT&lt;R[Rnn]</b> .....	31
RnnBXvalue .....	23	<b>TT&lt;Rnn</b> .....	31
Rnn <b>COS</b> .....	25	<b>TT&lt;value</b> .....	31
Rnn <b>QW</b> .....	25	<b>TT=0</b> .....	31
Rnn <b>R</b> .....	25	<b>TT&gt;R[Rnn]</b> .....	31
Rnn-R[Rmm].....	24	<b>TT&gt;Rnn</b> .....	31
Rnn <b>RAND</b> .....	25	<b>TT&gt;value</b> .....	31
Rnn-Rmm .....	24	<b>TTSR[Rnn]</b> .....	31
Rnn <b>SADy</b> .....	28	<b>TTSRnn</b> .....	31
Rnn <b>SEmm-xx.k</b> .....	26	<b>TTSvalue</b> .....	31
Rnn <b>SIN</b> .....	25	<b>Tvalue</b> .....	31
Rnn <b>SN</b> .....	26	<b>U*la</b> .....	32, 33
Rnn <b>SR[Rmm]</b> .....	25	<b>UA</b> .....	32
Rnn <b>SRmm</b> .....	25	<b>UE</b> .....	32

# MINILOG

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UE*la*	32	X>Rnn	34
UE[Rnn]	32	X>value	34
UEnn	32	X0-	35
UEP[name]	32	X0-^I	36
UEP[name]N*la*	33	X0+	35
UEP[name]Nnn	32	X0+^I	36
UEP[name]NR[Rnn]	33	X0+I	36
UEP[name]NRnn	33	X0-I	35
UERnn	32	XArR[Rnn]	37
UN*la*	33	XArR[Rnn]vvEnnz	37
Unn	32	XArRnn	37
UNnn	33	XArRnnvvEnnz	37
UNP[name]	33	XArvalue	37
UNP[name]N*la*	33	XArvaluevvEnnz	37
UNP[name]Nnn	33	XC	34
UNP[name]NR[Rnn]	33	XErR[Rnn]	37
UNP[name]NRnn	33	XErR[Rnn]vvEnnz	37
UNR[Rnn]	33	XErRnn	37
UNRnn	33	XErRnnvvEnnz	37
UP[name]	32, 33	XErvalue	37
UP[name]N*la	32, 33	XErvaluevvEnnz	37
UP[name]Nnn	32	XLr	36
UP[name]NR[Rnn]	32	XMA	35
UP[name]NRnn	32	XMD	35
UR[Rnn]	32	XPmmR	35
URnn	32	XPmmSR[Rnn]	35
X#E	34	XPmmSRnn	35
X#M	34	XPmmSvalue	35
X#N	34	XrR[Rnn]	36
X<R[Rnn]	35	XrR[Rnn]vEnnz	36
X<Rnn	35	XrR[Rnn]vvEnnz	36
X<value	35	XrRnn	36
X= I-	34	XrRnnvEnnz	36
X= I+	34	XrRnnvvEnnz	36
X=E	34	Xrvalue	36
X=H	34	XrvaluevEnnz	36
X=M	34	XrvaluevvEnnz	36
X=N	34	XS	37
X>R[Rnn]	34	XSN	37

## 4 List of DIN Instructions

The controller program can also be defined by the DIN instructions for process conditions and special functions. These standard instructions DIN 66025 can be used in one program with all the MINILOG instructions.

<i>Instruction</i>	<i>Meaning</i>
	<b>G Instructions (Process Conditions)</b>
<b>G00, G0</b>	Coordinate setting course Positioning with the speed as high as possible (high speed activation) without interpolation by parameter 14.
<b>G01, G1</b>	Set the linear interpolation
<b>G04Tnn, G4Tnn</b>	Program interrupts with term, programmed or defined in the controller The program continues automatically. n= in seconds with digits after the decimal point Abortion via input 2
<b>G05, G5</b>	Halt: the program waits for standstill of all axes and after that is continued
<b>G20Lnn</b>	Unconditional jump to line nn
<b>G20L+nn</b>	Unconditional jump by nn lines in + direction
<b>G20L-nn</b>	Unconditional jump by nn lines in – direction
<b>G20*label*</b>	Unconditional jump to *label*
<b>G20L*label*</b>	Unconditional jump to *label*
<b>G20LP[name]</b>	Unconditional jump to program name in line 1
<b>G21zLnn</b>	Conditional jump to line nn z=E or N
<b>G21zL+nn</b>	Conditional jump by nn lines in + direction z = E or N
<b>G21zL-nn</b>	Conditional jump by nn lines in – direction z = E or N
<b>G21z*label*</b>	Conditional jump to label z = E oder N
<b>G21zL*label*</b>	Conditional jump to label z = E or N

## MINILOG

<b>Instruction</b>	<b>Meaning</b>
<b>G21zLP</b> [name]	Conditional jump to program name in line 1 z = E or N
<b>G22L</b> nn	Call the subroutine program nn Subroutine is marked by G98Lnn in the program
<b>G22*</b> label*	Call the subroutine program *label*
<b>G22P</b> [name]	Call the subroutine program [name]
<b>G23L</b> nn	Stop the subroutine at once and return to line nn
<b>G23*</b> label*	Stop the subroutine at once and return to *label*
<b>G74</b>	Initialisation of all axes – direction
<b>G74</b> x	Initialisation of one axis x= X or Y
<b>G79L</b> nn	Automatic subroutine call at the end of the program line Subroutine is marked by G98Lxx in the program
<b>G80</b>	End of the automatic subroutine call G79
<b>G90</b>	Positioning absolute value in relation to the reference counter parameter 20
<b>G91</b>	Incremental positioning
<b>G92</b>	Set the absolute counter (zero offset) parameter 20
<b>G98L</b> nn	Subroutine beginning and declaration nn Subroutine name maximum 6 characters
<b>G99</b>	Subroutine end
	<b>M instructions (Additional Functions)</b>
<b>M00, M0</b>	Programmed halt The program is continued by setting input 2
<b>M01, M1</b>	Programmed halt, if input 3 is ON The program is continued by setting input 2
<b>M02, M2</b>	End of program
<b>M03, M3</b>	Spindle ACTIVATED, clockwise rotation output 1 on; output 2 off
<b>M04, M4</b>	Spindle ACTIVATED, counterclockwise rotation

<i>Instruction</i>	<i>Meaning</i>
	output 1 off; output 2 on
<b>M05, M5</b>	Spindle quick STOP output 1 off; output 2 off
<b>M07, M7</b>	Cooling 2 on output 3 off; output 4 on
<b>M08, M8</b>	Cooling 1 on output 3 on; output 4 off
<b>M09, M9</b>	Cooling off output 3 off; output 4 off
<b>M10</b>	Tool holder on; output 5 on
<b>M11</b>	Tool holder off; output 5 off
<b>M68</b>	Clamp component; output 6 on
<b>M69</b>	Unclamp component ; output 6 off

### 5 Parameters

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For operating a stepper motor controller several presettings as speed, acceleration ramps or waiting time are required. These presettings are called **Parameters**.

Default parameters are stored which can be used in several applications at delivery. You can read and edit these parameters with MiniLog-Comm.

Several counters are also contained in the list of parameters, which will be continuously actualized by the program. The counters can be read and some of them can be edited, too.

- For each axis separate parameters have to be set. Insert an X or Y to mark the axis in front of the parameter number (also valid: 1 or 2).

Example: XP15 is the acceleration ramp value for axis X.

- Parameters (e.g. speeds) may be modified several times within a program, too.
- Parameter values can be entered or read.
- P48 and P49 can only be read.
- P19 to P22 are counters. They will be actualized by the program during axis movement.
- P27 to P49 are special parameters for MCC-2.



## 5.1 List of Parameters


No.	Meaning	Default
<b>P01</b>	Type of movement 0 = rotational Rotating table, 1 limit switch for mechanical zero (referencing) 1 = linear for XY tables or other linear systems, 2 limit switches: Mechanical zero and limit direction – Limit direction +	0
<b>P02</b>	Measuring units of movement 1 = step 2 = mm 3 = inch 4 = degree	1
<b>P03</b>	Conversion factor for the thread 1 step corresponds to ... If P03 = 1 (steps) the conversion factor is 1. Computing the conversion factor: $\text{Conversion factor} = \frac{\text{Thread}}{\text{Number of steps per revolution}}$ <u>Example:</u> 4 mm thread pitch 200-step motor = 400 steps/rev. in the half step mode $\text{Conversion factor} = \frac{4}{400} = 0.01$	1
<b>P04</b>	Start/stop frequency The start/stop frequency is the maximum frequency to start or stop the motor without ramp. At higher frequencies, step losses or motor stop would be the result of a start or stop without ramp. The start/stop frequency depends on various factors: type of motor, load, mechanical system, power stage. The frequency is programmed in Hz.	400
<b>P05</b> <b>P06</b>	not used	
<b>P07</b>	Emergency stop ramp The frequency is programmed in 4000-Hz/sec-steps.	100 000

## MINILOG

No.	Meaning	Default
<b>P08</b>	$f_{\max}$ MØP (mechanical zero point) Run frequency during initializing (referencing) Enter in Hz (integer value)	4000
<b>P09</b>	Ramp MØP Ramp during initializing, associated to parameter P08 Enter in 4000-Hz/sec-steps	4000
<b>P10</b>	$f_{\min}$ MØP Run frequency for leaving the limit switch range Enter in Hz	400
<b>P11</b>	MØP offset for limit switch direction + Distance between reference point MØP and limit switch activation Unit: is defined in parameter P02	0
<b>P12</b>	MØP offset for limit switch direction – Distance between reference point MØP and limit switch activation Unit: is defined in parameter P02	0
<b>P13</b>	Recovery time MØP Time lapse during initialization Enter in msec	20
<b>P14</b>	$f_{\max}$ Run frequency during program operation Enter in Hz (integer value) (40 000 maximum)	4000
<b>P15</b>	Ramp for run frequency (P14) Input in 4000-Hz/sec-steps (4000 to 500 000 Hz/sec)	4000
<b>P16</b>	Recovery time position Time lapse after positioning Input in msec	20
<b>P17</b>	Boost (defined in P42) 0 = off 1 = on during motor run 2 = on during acceleration and deceleration ramp <u>Remarks:</u> The boost current can be set in parameter P42. You can select with parameter P17 in which situation the controller switches to boost current. P17 = 1 means, the boost current always is switched on during motor run. During motor standstill the controller switches to stop current.	0

No.	Meaning	Default
<b>P18</b>	not used	
<b>P19</b>	<p>Electronical zero counter</p> <p>Used for setting operating points. At standstill of the axis, P19 can be read or programmed during program execution.</p>	0
<b>P20</b>	<p>Mechanical zero counter</p> <p>This counter contains the number of steps referred to the mechanical zero (MØP). Can be read at axis standstill. If the axis reaches the MØP, P20 will be set to zero.</p>	0
<b>P21</b>	<p>Absolute counter</p> <p>Encoder, multi turn and also for single turn.</p> <p>The value of P22 is extended to P21 by software. The encoder counters have a fixed resolution, e.g. 10 bit (for single-turn encoders: the resolution is bits per turn), then the read value repeats. A saw tooth profile of the numerical values is produced during a continuous motor running. This course is "straightened" by software. P20 and P21 will be scaled to the same value per revolution by P3 and P39 and are therefore directly comparable, see P36.</p>	0
<b>P22</b>	<p>Encoder counter</p> <p>Indicates the true encoder position.</p>	0
<b>P23</b>	<p>Axial limitation pos. direction +</p> <p>If the number of steps is reached, the run in + direction is aborted.</p> <p>0 = no limitation</p>	0
<b>P24</b>	<p>Axial limitation neg. direction –</p> <p>If the number of steps is reached, the run in – direction is aborted.</p> <p>0 = no limitation</p>	0
<b>P25</b>	<p>Compensation for play</p> <p>Indicates the step number, the target position in the selected direction is passed over and afterwards is started in reverse direction.</p> <p>0 = no compensation for play</p>	0
<b>P26</b>	not used	
<b>P27</b>	<p>Initiator type</p> <p>0 = PNP normally closed contact (NCC)</p> <p>1 = PNP normally open contact (NOC)</p>	0

## MINILOG

No.	Meaning	Default
<b>P 28 to P33</b> not used		
<b>P34</b>	Encoder type 0 = no 1 = incremental 2 = serial interface SSI binary Code 3 = serial interface SSI Gray Code   Connect the <b>correct</b> encoder type! Do not parameterize an incremental encoder as SSI. Danger of damage!	0
<b>P35</b>	Encoder resolution for SSI encoder Enter max. encoder resolution in bit (max. 31Bit)	10
<b>P36</b>	Encoder function 0 = counter	0
<b>P37</b>	not used	
<b>P38</b>	Encoder preferential direction of rotation 0 = + (positive) 1 = - (negative)	0
<b>P39</b>	Encoder conversion factor 1 increment corresponds to ...	1
<b>P40</b>	Stop current in 0.1 A steps Values: 0 to 2.5 A Input: 0 to 25	2
<b>P41</b>	Run current in 0.1 A steps Values: 0 to 2.5 A Input: 0 to 25	6
<b>P42</b>	Boost current in 0.1 A steps Values: 0 to 2.5 A Input: 0 to 25	10
<b>P43</b>	Current delay time in msec	20
<b>P44</b>	not used	
<b>P45</b>	Step resolution 1 to 256 1 = Full step            10 = 1/10 step 2 = Half step            16 = 1/16 step 4 = 1/4 step            128 = 1/128 step 8 = 1/8 step            256 = 1/256 step	4
<b>P46</b>	Current Shaping (CS), also see appendix A 0 = Off            1 = On Recommended setting: P46 = 1	1



## 6 Programming Example

### 6.1 General

Line number	Program	Comment
LNo1	E^1R2R NN+1 X=H NE+1 XS H A1R2R	Reading 2 inputs, if both are 0 and the motor is running, then stop the motor if not, continue to next line. If the motor is out of action, reset output 1 and 2
LNo2	E^1S2R NN+1 X=H NN+1 XL+ A1S	If the first input is 1 and motor is out of action, then start running in + direction and set output 1
LNo3	E^1R2S NN+1 X=H NN+1 XL- A2S	Input 2 = running in – direction and set output 2, if the motor runs.
LNo4	E^3S NN+1 X=H NN+1 N+3	If input 3 = 1 and motor is out of action, then reference run on initiator, then continue program in line 1.
LNo5	E^4S NN-4 X=H NN-4 N+3	If input 4 = 1 and motor is out of action, then positioning relatively.
LNo6	N1	Return to line 1
LNo7	X0- A3S H A3R N1	Execute reference run on initiator – direction and wait until motor is out of action, then return to line 1. Set output 3 during reference run
LNo8	X+1000 A4S	Positioning 1000 steps in + direction. Set output 4 during positioning.
LNo9	E^5S1 NN+1 XS H A4R N1	Wait here until input 5 1, then stop motor and return to program start, if positioning is finished.
LNo10	X=H NN-1 A4R N1	Positioning finished ? If yes, then reset output 4 and return to line 1

### 6.2 A/D Converter

Program	Comment
*START*	
R2SAD1	Register 2 is set with AD card 0 Ch 1
R3SAD2	Register 3 is set with AD card 0 Ch 2
R2W2	The value of register 2 is displayed in line 2
R3W3	The value of register 3 is displayed in line 3
N*START*	Jump back to Start

## 7 Storing Programs, Parameters and Registers

Programs and parameters can be edited with MiniLog-Comm, transferred to the controller and stored. During program run registers and counters can be modified by the program. As long as the controller is powered these data are stored. After switching off the controller, these data will be handled dependent on the built-in type of memory components:

<b>Flash-EPROM Memory</b>	<p>Register or counter values modified by the program will <u>not</u> be stored when you switch off the controller.</p> <p>If these data are further required, they should be stored with MiniLog-Comm before switching off and transmitted to the controller again.</p>
<b>RAM Memory</b>	<p>The first 100 registers are stored in a nonretentive RAM</p> <p>Advantage: fast access</p> <p>Disadvantage: data get lost when powered off</p> <p>As from register 101 the data are stored in a serial RAM <b>(SRAM)</b>:</p> <p>Advantage: data remain stored after powered off and are available after power on</p> <p>Disadvantage: slow access</p>

## 8 Current Shaping CS

Current Shaping (CS) is a circuitry method for delivering a true phase current which corresponds for a wide range of frequencies to a selected current shape.

If the stepper motor is driven without CS, the true current differs from the specified current, even in the lower speed frequencies.

The 1/20 sine wave mode results in a current deviation as shown in the following figure, for average speed:

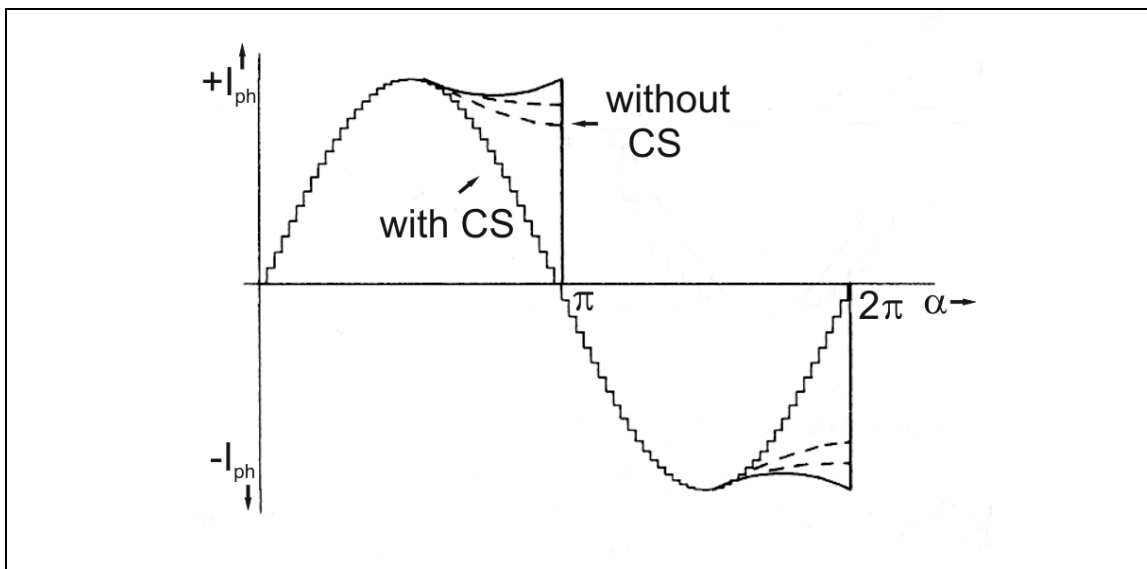


Fig. 1: Current shaping CS

These typical deformations can be observed for all types of curves. They are caused by the stepper motor inductance and the generator feedback which increases with the motor speed.

The resulting 'current queue' makes precise current regulation possible by Current Shaping (CS= 4 quadrant current regulation), only. The amplitude of the 'current queue' varies strongly remarked during one revolution and may provoke a motor resonance effect which causes step losses or desynchronization of the motor.

If the CS function is activated, the 'current queue' disappears and the resulting current is close to the ideal shape.

We recommend to use CS in higher current and speed frequency ranges.

The CS function can be activated by the parameter P46 (see chap. 5.1.).



## 9 Index

### A

A/D converter 28, 52

Addressing mode  
 direct 5  
 indirect 5  
 with label 6

Addressing  
 with label 6

Addressing mode  
 Indirect 6

Axis Instructions  
 Free Running 36  
 Initialization 35  
 Power stages 34  
 Read/load parameter 35  
 Status request 34  
 Stop 37  
 Wait 34

### B

Baudrate  
 read 13  
 set 13

Broadcast 7

### C

Checksum 8

Compensation for play 49

Condition byte 6

Current Shaping 54

### D

DIN instructions 43

Display instruction 33

### E

ELØP 37

### F

Flash-EPROM 53

### I

Inputs  
 Conditional link 11  
 Logical AND 11  
 MCC-1 12  
 Read status 12

Instruction code 4

Interface 46

### J

Jump instructions  
 conditional 16  
 relative 15

### L

Label 6

Limit switch 47

### M

MiniLog-Comm 5, 53

MØP 36

### O

Outputs  
 MCC-1 12  
 read 9  
 Reading 9  
 set 9

### P

Password  
 read activation status 17  
 Set activation status 17

Positioning  
 absolute 37  
 in relation to ELØP 37  
 in relation to MØP 37  
 relative 36

Process conditions 43

Program and data management  
 read program 19

Program Call  
 Ending 17

Programname 6

### R

RAM 53  
 Contents read 14

Reference search run 35

Register  
 Shifting 21

Register instructions  
 Arithmetical operations  
*Cosinus* 25  
*Random number* 25  
*Sinus* 25  
*Square root* 25

## MINILOG

---

*Tangent* 25  
write with A/D converter values 28  
write with decimal value 25  
write with line number 26  
write with line number emergency stop 26

Registers 20, 53

Reset Controller 10

### **S**

Send telegram 8

SRAM 53

Standard functions 43

Start-/Stop frequency 47

Subroutines

Break-off 32

Call 32

conditional call 32

End 32

Synchronous start 30

System Status (only computer mode)

decimal 30

### **T**

Time loops 31

### **V**

Version request 14

### **W**

Write instruction via serial interface 10

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